

VIVEKANAND EDUCATION SOCIETY'S INSTITUTE OF TECHNOLOGY



DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

Departmental Report

(1st November 2022 to 30th April 2023)



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

INDEX

Sr. No.	Contents
1	Action plan & Action taken report
Faculty Contribution	
1	Orientation Program conducted by Faculty
2	Orientation Program attended by Faculty
3	Workshops/Courses conducted by Faculty
4	Workshops/Courses attended by Faculty
5	NPTEL certification courses
6	National Initiative for Technical Teachers Training (NITTTR) courses attended by faculty
7	Project Exhibition
8	Research Paper Publications
9	Research Forum - Experts talk
10	Patent Publication
11	Faculty Interaction with outside world
Student Achievements	
1	Academic Excellence
2	Placement Statistics
4	Competitive Exams
5	Scholarship
6	Industrial Projects
7	Industrial Visits
8	Workshops/Seminars attended by students
9	Internship attended by students
10	Student Paper Publications
	Project Competitions
11	Participation in co-curricular activities 1. Inter-college 2. Intra-college
12	Participation in extracurricular activities 1. Sports events 2. Cultural, Music & Literature events
13	Tinkerer's lab activities



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

Action plan & Action taken report

S. No.	Concern	Action plan	Action Taken
1.	Academic Planning	Preparing departmental academic calendar for the even semester	<p>1. Departmental academic calendar is prepared and shared with all the faculty members. EXTC Academic Calendar Even sem 22-23</p> <p>2. From Even semester of AY:2022-23, department has prepared project calendar foEXTC Deptt_Even sem_22-23_Calendar SE, TE & BE project weekly logbook, review and assessment EXTC Project Calendar even sem 22-23</p>
2.	Academic results	<p>1. Remedial lectures for the subjects having less passing percentage.</p> <p>2. Identification of slow learners & extra activities for them to improve results.</p>	<p>1. For Electronic Devices & Digital Circuit Design subject remedial lectures were arranged. Remedial Schedule EXTC even sem 22-23</p> <p>2. Many faculty identified slow learners in their subject & tasks were assigned to them with study material. Sample Report Slow lerner D14B_Skill Lab</p>
3.	Departmental activity publicity & documentation	1. All the activities conducted by the department should be posted on college	1. Department posts all activities on college website & social media platforms



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

		<p>website & on social media platforms for publicity.</p> <p>2. Reports of all the activities conducted should be prepared in standard format and should be submitted to the Head of the department.</p>	<p>like Twitter, Facebook, Instagram & YouTube.</p> <p>2. A standard report format is given to all faculty members to be submitted to the Head of the department & Data bank of the EXTC department.</p>
4.	Research promotion activities	To conduct expert talks under research forum	Research forum organized a session on "Design and Implementation of Chaos-based Image Encryption and its Cryptanalysis" by Dr. Rasika Naik
5.	Research Publications	Increase the number and quality of research papers by arranging workshops.	<p>1. Workshop on "Latex" by Dr. C. S. Rawat.</p> <p>2. Workshops on "How to write technical papers" by Dr. M. D. Patil</p>
6.	Project Innovations, Industry Projects	<p>1. To get projects from industry.</p> <p>2. To improve the quality of projects by arranging project competitions and taking the feedback from EXTC alumni working in the industry.</p>	<p>1. Department has got three FPGA based project from Tata Institute of Fundamental Research (TIFR)</p> <p>2. Department has called EXTC alumni for the review of SE & TE mini projects and their feedback was taken to improve the quality of projects.</p>
7.	Project Competition	<p>1. To organize project competitions for the students.</p> <p>2. To encourage students to</p>	<p>1. Department has organized two project competitions IDEathon 2023 & Avinya 2023 on Technology day for the students.</p>



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

		participate in project competitions organized by other institutes.	2. Mentor encouraged students to participate in project competitions as a result EXTC students participated & won prizes in National level project competition at SPIT, CIIA Project exhibition & LBS-I2I Project competition-2023.
8.	Major/Minor Research Grant Proposals / Patent	Department faculty should file patents & apply for the research grants.	Mrs. Manisha Joshi has filed two patents for "A function generator embedded with ANN" & "Power-low type activation function for computing system"
9.	Industrial Visits / Industry interaction	To arrange industrial visits for SE, TE & BE students.	1. One day industrial visit to Tata Communications Ltd, Dighi, Pune for BE & TE students on 03/02/2023 2. One day industrial visit to Tarapur Atomic Power Station, Tarapur for SE students on 24/02/2023
10.	Student Internship	To have industry-like experience & to Develop Better Professional Skills, the department should encourage students for industry internship during their semester breaks.	Department provides request letters & bonafide certificates to students for industry internship during semester break..
11.	Certification Courses	1. To organize certification courses in association with the industry.	1. Department has organized an Online certification course on "Hands-on Cyber Security



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

		<p>2. To complete NPTEL certification course (Faculty & Students)</p> <p>3. NITTTR certification by faculty</p>	<p>Essentials” in association with TeleNetworks Communication Pvt. Ltd.</p> <p>2. Dr. Rasika Naik completed NPTEL certification course on “NBA Accreditation and Teaching and learning in Engineering (NATE)”</p> <p>3. Mrs. Neeta Chavan completed module -7 (Creative Problem Solving , Innovation and Meaningful R & D” of NITTTR certification</p>
12.	Placement activities /Competitive Exams/ Higher Studies/Entrepreneurship Initiatives	To make students industry ready by arranging various activities under GRIN:Get Ready For Industry by EXTC placement team.	Placement cell of department conducted various activities like online aptitude quiz, session on employability, eligibility, resume writing, skill set analysis, etc
13.	Orientation Programs	Conducting & participating in orientation programs for new subjects added to the curriculum on behalf of University of Mumbai.	<p>1. Mr. Gaurav Tawde conducted an orientation program for the subject “Satellite & Nano Satellite Communication (ECCDLO8022)BE-EXTC/S emester VIII”</p> <p>2. Four faculty members attended orientation programs for semester VIII subjects.</p>
14.	Workshops/Short Term Training	To conduct STTP, FDP & workshops for faculty training	All faculty members have attended FDP/STTP/Workshop.



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

	Programs/Faculty Development Programs for faculties	to update them in the field of technical education to introspect and learn techniques that can help prepare students for active and successful participants in a knowledge society.	
15.	Participation in Workshops/Short Term Training Programs by the students	To teach students hands-on skills, the department should conduct & encourage students to take part in Workshops/ STTP.	Department's Tinkerer's lab conducted STTP on "Trends in FPGA Design & Prototyping with VERILOG " for TE students.
16.	Alumni Interaction	To involve alumni for Placements, Industry engagements, Syllabus Reviews & Feedback	Alumni were invited for Mini project reviews, Convocation and are actively involved in the Department Advisory Board (DAB).
17.	Students participation in co-curricular & extracurricular activities	To bring social & intellectual skills, moral, cultural & ethical values, personality development, and character advancement in students, the department should encourage students to take part in various activities organized by professional committees & VESIT council.	<ol style="list-style-type: none">1. Students actively participated in the technical fest "Praxis" organized by VESIT.2. In Cultural fest "Utsav" & "Illusion", Sports events "Indoor & Outdoor Sphurti", EXTC students showed their remarkable presence.



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

Detailed Report

Various activities conducted in the Department of Electronics & Telecommunication, from 1st November 2022 to 30th April 2023 have been categorized as mentioned below.

Faculty Contribution

1. Orientation Programs conducted by Faculty

Sr. No	Faculty Name	Conducted By	Course description	Organized by	Date
1	Dr.Saylee Gharge	Department of EXTC	Satellite & Nano Satellite Communication (ECCDLO8022) BE-EXTC/Semester VIII	V.E.S. Institute of Technology, Chembur	6/2/2023
2	Mr. Gaurav Tawde	Department of EXTC	Satellite & Nano Satellite Communication (ECCDLO8022)BE-EXTC/Semester VIII	V.E.S. Institute of Technology, Chembur	6/2/ 2023

2. Orientation Programs attended by Faculty

Sr. No	Name of Faculty	Conducted By	Course description	Organized by	Date
1	Dr. Rasika B. Naik	Vidyavardhini's College of Engineering and Technology, Vasai Road	Wireless Network	Vidyavardhini's College of Engineering and Technology, Vasai Road	23rd Jan 2023
2	Dr. Ranjan Bala Jain	Terna Engineering College, Nerul, Bharati Vidyapeeth College of Engineering, Khargha	Optical Communication & Networks	Terna Engineering College, Nerul, Bharati Vidyapeeth College of Engineering, Kharghar and	14th Jan 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

		r and Saraswati College of Engineering, Kharghar		Saraswati College of Engineering, Kharghar	
3	Dr. Nandini Ammanagi	SIES Graduate School of Technology, Nerul ,Navi Mumba	Internet Communication Engineering	SIES Graduate School of Technology, Nerul ,Navi Mumba	15th July 2022
4	Mr. Gaurav Tawde	Terna Engineering College,Nerul, Bharati Vidyapeeth College of Engineering, Kharghar and Saraswati College of Engineering, Kharghar	Optical Communication & Networks	Terna Engineering College,Nerul, Bharati Vidyapeeth College of Engineering, Kharghar and Saraswati College of Engineering, Kharghar	14th Jan 2023

3. Workshop/Courses/talk conducted by faculty

S. No.	Name of Faculty	Workshop/course/talk conducted by faculty	Organized by	Date
1	Dr. Chandan Singh Rawat	Workshop on LATEX	Department of Electronics & Telecommunication Engineering, VESIT	25/02/2023 & 28/02/2023
2	Dr. M. D. PAtil	Workshop on How to write Technical Paper	Department of Electronics & Telecommunication Engineering, VESIT	4/03/2023
3	Dr. Nandini Ammangi & Mr. Mrugendra M Vasmatkar	STTP on "Trends in FPGA Design & Prototyping with VERILOG"	EXTC Tinkerer's LAB In association with VESIT-IQAC, VESIT-IIC, Coreel Technologies & VESIT-ISA	2nd Jan-7th Jan 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

4. Workshops/Courses attended by faculty

Sr. No.	Attended By	Course description	Organized by	Date
1	Dr. Chandan Singh Rawat	Augmented and Virtual Reality	Department of Information Technology, Vishwakarma Institute of Information Technology, Kondhawa, Pune	20-24 Feb 2023
2	Dr. Monali Chaudhari	UGC Approved Short Term Professional Development Programme Under Pandit Madan Mohan Malaviya National Mission on Teachers and Teaching	Indira Gandhi National University Staff Training and Research Institute of Distance Education	January 20--29, 2023-
3	Dr. Monali Chaudhari	"Industry 4.0: Strategic & Technological Perspectives for Indian Capital Goods Industries"	Visvesvaraya National Institute of Technology, Nagpur	8th to 12th December 2022
4	Mrs. Neeta Chavan	"SOCIAL MEDIA ANALYTICS"	Department of Computer Engineering in association with VESIT -IQAC	2nd January to 6th January 2023.
5	Mrs. Ashwini Sawant	ISTE approved STTP on "SOCIAL MEDIA ANALYTICS"	Department of Computer Engineering in association with VESIT -IQAC	2nd January to 6th January 2023.
6	Dr. Rasika B. Naik	"Industry 4.0: Strategic & Technological Perspectives for Indian Capital Goods Industries"	Visvesvaraya National Institute of Technology, Nagpur	8th to 12th December 2022



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

7	Mrs. Manisha Joshi	ISTE approved STTP on "SOCIAL MEDIA ANALYTICS"	Department of Computer Engineering in association with VESIT -IQAC	2nd January to 6th January 2023.
8	Dr. Ranjan Bala Jain	ISTE approved STTP Programme on "Quantum Computing"	Department of Electronics & Telecommunication Engineering, Agnel Charites' Fr. C. Rodrigues Institute of Technology	5th Jan-9th Jan 2023
9	Mr. Shobhit K. Khandare	ISTE approved STTP on "SOCIAL MEDIA ANALYTICS"	Department of Computer Engineering in association with VESIT -IQAC	2nd January to 6th January 2023.
10	Dr, Nandini Ammanagi	Industrial IOT: Security Issues, Challenges & Solutions	METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY	26/12/2022 to 31/12/2022
11	Dr, Nandini Ammanagi	"Industry 4.0: Strategic & Technological Perspectives for Indian Capital Goods Industries"	Visvesvaraya National Institute of Technology, Nagpur	8th to 12th December 2022
12	Mrs. Anuradha Jadiya	ISTE approved STTP on "SOCIAL MEDIA ANALYTICS"	Department of Computer Engineering in association with VESIT -IQAC	2nd January to 6th January 2023.
13	Mrs. Jyoti Bagate	ISTE approved STTP on "SOCIAL MEDIA ANALYTICS"	Department of Computer Engineering in association with VESIT -IQAC	2nd January to 6th January 2023.
14	Mrs. Arti Sawant	ISTE approved STTP on "SOCIAL MEDIA ANALYTICS"	Department of Computer Engineering in association with VESIT -IQAC	2nd January to 6th January 2023.



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

15	Dr.Saylee Gharge	ISTE approved STTP on "SOCIAL MEDIA ANALYTICS"	Department of Computer Engineering, VESIT	2nd Jan to 6th Jan 2023
16	Mr.Chintan Jethva	ISTE approved STTP on "SOCIAL MEDIA ANALYTICS"	Department of Computer Engineering, VESIT	2nd Jan to 6th Jan 2023
17	Mrs. Himali Patel	ISTE approved STTP on "SOCIAL MEDIA ANALYTICS"	Department of Computer Engineering in association with VESIT -IQAC	2nd January to 6th January 2023.

5. NPTEL Certification by faculty

Sr. No.	Name of Faculty	NPTEL Course description	Organized by	Period
1	Dr. Rasika Naik	Registered for NBA Accreditation & Teaching and learning in Engineering (NATE)	IISc Bangalore, MSRIT	23 Jan 2023 to 14 Apr 2023

6. National Initiative for Technical Teachers Training courses attended by faculty

Sr. No.	Attended By	Module No.	Course description
1	Mrs. Neeta Chavan	7	Creative Problem Solving, Innovation and Meaningful research and development

7. Project Competition/Exhibition

Sr. No.	Project description	Date
1	IDEathon 2023	8/4/2023
2	Avinya 2023	20/3/2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

8. Research Paper Publications

Sr. No	Author List	Title of Paper	Date	National/International	Journal/Conference	Organized By	Publisher
1	Dr. Chandan Singh Rawat	Use of AI based cloud Certificate Authentication for Travel Concession	11-12 Jan 2023	International	Conference	4th International Conference on Mobile computing and sustainable Informatics (ICMCSI 2023) 11-12 Jan 2023	Springer
2	Dr. Chandan Singh Rawat	Oil spill detection using Synthetic aperture RADAR	10-12 Nov 2022	International	Conference	IoT in social, Mobile, Analytics and cloud (I-SMAC 2022) 10-12 Nov 2022	IEEE Xplore
3	Mr. Gaurav Tawde	Recognizing Handwritten Texts	March, 2023	International	Journal	--	IJERT
4	Mrs. Ashwini Sawant	IoT powered Smart Stroller	March 2023	International IEEE	Conference	--	IEEE Xplore
5	Dr. Rasika B. Naik	Virtual Lab on Oscillators	March 2023	International	Conference	20th International Conference	REV2023 – in Thessalon



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

						on Remote Engineering and Virtual Instrumentation.	iki, Greece
6	Mrs. Manisha Joshi	Using fractional derivative in learning algorithm for artificial neural network: Application for salary prediction	November 2023	International IEEE	Conference	IEEE Bombay Section Signature Conference (IBSSC), Mumbai,	IEEE Xplore
7	Dr. Rasika B. Naik	A review on Smart Mirror	April 2023	International	Journal	International Journal of Innovative research in Science, engineering and Technology (IJRSET 2023)	
11	Dr. Nandini Ammanagi	Blood Sugar Level detection and storage using Split Ring Resonator and Python	22-24 february 2023	International		2023 Fifth IEEE International Conference on Electrical, Computer and Communication	IEEE



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

						Technologies 22 - 24, February 2023 www.icecct.com	
12	Mrs. Manisha Joshi	A survey of fractional calculus applications in artificial neural networks	25th April 2023	International	Journal	Artificial Intelligence Review, Springer, 2023	Springer
13	Dr. Rasika B. Naik	A Review on Automated Waiters	29th April 2023	International	Conference	11th International Conference on Emerging Trends in Engineering & Technology Signal and Information Processing	IEEE
14	Dr. Nadir N. Charniya	M-Vahitaram: AI-Based Android Application for Automated Crowd Control Management in Bus Transport Service,	16 Nov 2022	International	Conference	Springer Nature Proceedings of Emerging Trends and Technologies on Intelligent Systems pp 141-154	Springer



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

9. Research Forum - Experts talk

Sr. No	Date	Duration	Name of Expert	Designation & Organization	Topic	Attended By
1	11/03/2023	1Hr	Dr.Rasika Naik	Assistant Professor, EXTC, VESIT	Design & Implementation of Chaos-based Image Encryption and its Cryptanalysis	VESIT Staff

10. Patent Publication

Sr. No	Patent Inventor Name	Patent Name	Patent Co-Inventor	Patent Status
1	Mrs. Manisha Joshi	Power-Law activation function based computing system	Nikhil Oza, Vrithik Zathani, Vishal Prajapati, Saket Bhosle, Vivekanand Education Society's Institute of Technology	filed
2	Mrs. Manisha Joshi	A function generator embedded with ANN	Harshala Dalal, Bhavika Methwani, Shubham Verma, Vedha Nayak, Vivekananda Education Society Institute of Technology (VESIT)	filed

11. Faculty Interaction with outside world

Sr. No.	Name of Faculty	Role	Details of interaction	Organized by	Dates of interaction
1	Dr. Monali Chaudhari	Research paper reviewer	5 th IEEE International Conference on "Emerging Smart Computing & Informatics" (IEEE ESCI-2023)	All India Shri Shivaji Memorial Society's Institute of Information Technology	1st- 3rd March 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

2	Dr. Monali Chaudhari	Research paper reviewer	5 th IEEE - International Conference on Advances in Science & Technology (IEEE-ICAST - 2022)	K. J. Somaiya Institute of Engineering and Information Technology, Sion, Mumbai,	2nd and 3rd of December 2022.
3	Dr. Monali Chaudhari	Research paper reviewer	International Conference on Advancement in Computation & Computer Technologies, 2023 (InCACCT-2023) IEEE Conf. Record No: 57535X	Chandigarh University	05th – 06th May 2023
4	Dr. Rasika B. Naik	Research paper reviewer	International Conference on Advancement in Computation & Computer Technologies, 2023 (InCACCT-2023) IEEE Conf. Record No: 57535X	Chandigarh University	05th – 06th May 2024
5	Dr. Rasika B. Naik	Research paper reviewer	IEEE International Conference on Current Regards Development in Engineering and Technology (CCET-2022).	SAGE University, Bhopal	Dec 2022
6	Dr. Rasika B. Naik	Paper Setter for University (other than MU)	For Subject- Computer Communication and Networks (CCN) SEM-VI	Pillai College of Engineering, New Panvel	May 2023
7	Dr. Rasika B. Naik	Paper Setter for University (other than MU)	–	RAIT, Navi Mumbai	Nov 2022
8	Dr. Rasika B. Naik	Reviewer for AICTE	For Subject- Basics of Electronics	AICTE	Nov 2022



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

		Technical Book Translation.			
9	Mrs. Manisha Joshi	Paper Setter for University (other than MU)	–	RAIT, Navi Mumbai	May 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

Students Achievements

1. Academic excellence

Statistical graphs illustrating the performance of the students by the end semester results from University of Mumbai for Academic year 2022-23 odd semester.



Academic result for Odd Semester (2022-23)

2. Placement statistics

Campus placement holds are of utmost importance for students in today's competitive world.

For 2022-23 EXTC batch, a total of 19 companies visited the college and a total of 69 offers from various companies like JPMC, Deloitte, Acumen Security (Acucert LLP), Accenture, LTI, Capgemini, Quantiphi, Kotak Mahindra Bank etc. were given. These include 34 dream job offers. Details are as follows:

S.No	Company	Number of Job offers =69	Salary Lacs/Annum
1	JPMC	4	17.75
2	Carwale	1	9
3	Quantiphi	4	8.5
4	Deloitte	16	7.6
5	Selec Control	1	6
6	Acumen Security (Acucert LLP)	5	6
7	Accenture	4	4.5
8	Ernst & Young	3	6.37
9	FYND	1	13.5
10	ITC Infotech	3	4.25
11	LTI	4	6.5, 5 and 4



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

12	Apport Software solution Pvt Ltd (QuickSell)	1	15
13	Kotak Mahindra Bank	5	10
14	L & T GET	3	6
15	TATA Consultancy Services Limited	4	3.36
16	Godrej & Boyce	3	6.2
17	Games 24 X 7	2	11
18	Kotak Securities	3	6.5
19	Capgemini	2	4.25

3. Competitive Exams

S. No.	Name of Student	Exam Name
1	Ameya Padwad	GRE
2	Devesh Ramesh	TOEFL
3	Risa Samanta	GRE
4	Abhishek Ajit Bhingarde	GATE

4. Scholarship given to students

Sr. No.	Name Of Student	Class	Scholarship Received	Amount
1	Khan Md. Ayan	D19B	Vivekanand Jyoti Scholarship	25,000
2	Sumit Sanjiv Pawar	D9A	Jadavpur Alumni Association Trust	20,000
3	Akanksha Dilip Gawade	D19B	Student Aid Scholarship	15,000
4	Snehal Patil	D9B	Student Aid Scholarship	15,000
5	Ayush Pramod Kotkar	D14B	Student Aid Scholarship	15,000
6	Pritam Mahadeo Dhamak	D9B	Student Aid Scholarship	15,000
7	Shreya Ravindra Dhamane	D19A	Student Aid Scholarship	15,000
8	Harshal Talreja	D9A	Student Aid Scholarship	15,000
9	Siddhesh Deepak Shirshivkar	D9A	Student Aid Scholarship	15,000
10	Nafees Akhter	D19B	Student Aid Scholarship	15,000



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

5. Industry Projects

Sr. No.	Name of Industry	Title of Project	Name of Students	Project Mentor
1	Development of FPGAS based systems for radiation monitoring system	Development of FPGAS based systems for radiation monitoring system	Tanmayee Bochare, Kaustubh Pawar, Shantanu Deshmukh Milind Sawant	Mrs. Jyoti B
2	Development of FPGAS based systems for radiation monitoring system	Development of FPGAS based systems for radiation monitoring system	Dsouza Alrich , Pratik Patkar, Atharva Petkar, Aayushi Rajeshirke	Mr. Mrugendra Vasmatkar
3	Development of FPGAS based systems for radiation monitoring system	Development of FPGAS based systems for radiation monitoring system	Vaishnavi Amritwar, Ashutosh D., Madhura Ghanwat, Atharva Nayak	Mr. Mrugendra Vasmatkar

6. Industrial Visit

Sr. No.	Company Name	Company Location	Date	Duration
1	Tarapur Atomic Power Station	Tarapur, Boisar	24/02/2023	1 day
2	Tata Communications Ltd, Dighi, PUNE	Dighi, Pune	03/02/2023	1 day

7. Workshops/Seminars attended by students

Sr. No.	Attended By	Course description	Organized by	Date
	Pooja Narayanan	ESP32 and Micropython workshop, ISA	ISA, VESIT	6 -7 February, 2023
	Gaurang Kedare	Hands-on CyberSecurity Essentials Workshop	EXTC VESIT	February, 2023
	Siddhesh Shirshivkar	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

	Aaroh Srivastava	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Aditri Vishwas	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Adwait Panadi	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Akash Neharkar	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Anshkumar Agrahari	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Anuja Kulkarni	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Khushi Purohit	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Pranav Jha	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Prasad Gourshettiwar	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Shubham	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Suyog Bharsakle	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Vedant Vijay Patil	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Vikas A Dhakane	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Aditya Nama	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Bhakti Daga	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Mayur Santosh Pawar	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Ninad Joshi	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Preeti shivaji Sonawane	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Rishika Chaubal	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Shreyas kale	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Siddharth Patil	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202
	Snehal Patil	UI/UX Workshop, IEEE	IEEE, VESIT	02-03 February, 202



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

8. Students Internship

Sr. No.	Attended By	Course description	Company Name	Date from	Till Date
1	Aryan Chaudhary	IoT and AI based project	VESIT	1/12/2022	1/1/2023
2	Tejas Sharma	Image Processing and AI based project	VESIT	1/12/2022	1/1/2023
3	Hritika Mulay	Design of analog comparator	VESIT	1/12/2022	1/1/2023
4	Radhika Damle	VLSI implementation of DSP algorithm using MATLAB simulink	VESIT	1/12/2022	1/1/2023
5	SHREYAS SARANG	VLSI implementation of DSP algorithm using MATLAB simulink	VESIT	1/12/2022	1/1/2023
6	Pooja Ghadage	VLSI implementation of DSP algorithm using MATLAB simulink	VESIT	1/12/2022	1/1/2023
7	Milind Sawant	Prototyping if FPGA using VERILOG Programming	VESIT	1/12/2022	1/1/2023
8	Shantanu Deshmukh	Prototyping if FPGA using VERILOG Programming	VESIT	1/12/2022	1/1/2023
9	Kaustubh Pawar	Prototyping if FPGA using VERILOG Programming	VESIT	1/12/2022	1/1/2023
10	Alrich Xavier Dsouza	Prototyping if FPGA using VERILOG Programming	VESIT	1/12/2022	1/1/2023
11	AAYUSHI SANTOSH RAJESHIRKE	Prototyping if FPGA using VERILOG Programming	VESIT	1/12/2022	1/1/2023
12	Pratik Patkar	Prototyping if FPGA using VERILOG Programming	VESIT	1/12/2022	1/1/2023
13	Atharva Petkar	Prototyping if FPGA using VERILOG Programming	VESIT	1/12/2022	1/1/2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

14	Tanmayee Bochare	Prototyping of FPGA using VERILOG Programming	VESIT	1/12/2022	1/1/2023
15	Udit Koli	Design of analog comparator	VESIT	1/12/2022	1/1/2023
16	Sharvari Patwardhan	Design of analog comparator	VESIT	1/12/2022	1/1/2023
17	Pradeesh Reddy	Design of analog comparator	VESIT	1/12/2022	1/1/2023

9. Student paper publication

Sr. No.	Student Name	Title of the paper	Name of Organization	Level
1	Aishvarya Birambole Isha Chavan Saakshi karkera Risa Samanta	Design and Development of Smart Pill Dispenser	Sardar Patel College of Engineering, Mumbai in association with SPIT, Mumbai, Capgemini, SAE India WS, IE(I), IIIE, FELA and technical collaboration with American Institute of Physics & IEEE Bombay Sec	International
2	Aishvarya Birambole Isha Chavan Saakshi karkera Risa Samanta	Comparative Study of Machine Learning Algorithms for Prediction of Polycystic ovary syndrome	Sri Eshwar College of Engineering, Tamil Nadu	International
3	Ameya Dikshit Janhavi Bhutki Ahona Chattopadhyay Pratham Angre	Image to Text and Based Learning Aid for Children	IEEE 8th I2CT 2023	International
4	Abhishek Bhatia Avantika Shetty Nafees Akhter,	IoT powered smart stroller	IEEE-ICACCS 2023	International



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

	Shweta Bhabal			
5	Yogesh Inamdar Sakshi Gupta Rushikesh Palekar Mihir Ranje	Blood Sugar Level detection and storage using Split Ring Resonator and Python	5th IEEE International Conference on Electrical, Computer and Communication Technologies 22-24 Feb2023	International
6	Gargi Khanvilkar Akshata Deshpande Gunjan Surekha Sreya Dhamane	A Review on Smart Mirror	International Journal of Innovative Research in Science, Engineering and Technology (IJIRSET)	International
7	Parth Rane Vinghesh Poojari Niranjan Vajjramusti Rohan Bhamre	A Review on Smart Waitor	2023 11th International Conference on Emerging Trends in Engineering & Technology Signal and information Processing (ICETET-SIP-23)	International

10. Project Competition

Sr. No.	Name of Students	Name of Competition	Organized by	Prize	Level	Date
1	Farheen Khan Heeral Chumber Riddhi Joshi Arunkumar Marippan Suresh Bairi	SPIT Innovation Cup	SPIT	5000 Rs for Prototype	National	8th April, 2023
2	Ishan Kharat Kartik Avhad Sumit Jadhav Devesh Ramesh Dhaval Mahajan	SPIT Innovation Cup	SPIT	5000 Rs for Prototype	National	8th April, 2023
3	Abhishek Bhatia Avantika Shetty	Peripheratho n 1.0 IOT	SPIT	2500 Rs for Prototype	National	15th April 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

	Shweta Bhabal Nafees Akhter Gaurang Kedare	Hardware Hackathon				
4	Farheen Khan Heeral Chumber Riddhi Joshi Arunkumar M.	PRAKALP, IIC IEEE	FrCRCE , Bandra	Winner	National	
5	Neha Makattil Vasudha Sasikumar Varun Arya V Anuparvathy	TANTRAGY AN'23	LTCOE	Winner	National	28th March 2023
6	Mcvean Soans Prajna Moorthy MRUNAL M. SAPTARSHI DAS	Tantragyan 2023	LTCOE	Winner	National	28th March 2023
7	Isha Chavan AISHVARYA B. RISA SAMANTA SAAKSHI K.	Ideathon	Online IEEE TPAC Bombay Section	Winner	Intercollege	–
8	Priyanshu Bhagat Prajakta Gurav Arjun Satheesh Gautam govindaraju	Oscillations - 2023	SAKEC	Participation	Intercollege	5 April 2023
9	Priyanshu Bhagat Prajakta Gurav Arjun satheesh, Gautam govindaraju	CONCEPTI ON - 2023	SFIT	Participation	Intercollege	11 April 2023
10	Neha Makattil Vasudha Sasikumar, Varun Arya, Anuparvathy	CIIA-2 (2023)	CIIA Exhibition at Nehru Centre	Participation	National	1st - 3rd February 2023
11	Ameya Dikshit Ahona C.	CIIA-2	CIIA Exhibition	Participation	National	1st - 3rd February



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

	Pratham A. JANHAVI BHUTKI		at Nehru Centre			2023
12	Shripad Kulkarni Rohan Giri Adarsh P.S Bhavesh Khati	XZIBIT-202 3 National Level Project Competition	Excelsior Education Society's K.C. College of Engineering & Managemen t Studies & Research , Thane	Winner	National	8th and 10th April 2023
13	Shripad Kulkarni Rohan Giri Adarsh P.S Bhavesh Khati	Bit n Build	Google Developer Student Clubs of CRCE & UMIT	Participation	National	28th and 29th Jan 2023
14	Shripad Kulkarni Rohan Giri Adarsh P.S, Bhavesh Khati	IEEE Ideathon	IEEE Bombay Section	Pre- qualifier	National	12th November 2022
15	Rohan Bhamre Parth Rane Niranjan Vajramusti, Vignesh Poojari	Oscillations 2023	Shah and Anchor Kutchi Engineering College	Participation	Intercollege	
16	Mevean Soans Prajna Moorthy MRUNAL	Xzibit'23	KCCOE	Winner	National	10 April 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

	MURUDKAR SAPTARSHI DAS					
17	TRISHA GAUR ADVAIT KULHADA SHARVANI SAWANT SHUBHAM SANGANI	AVINYA, 2023 Technology Day	VESIT	Participation	Intracollege	20th March, 2023
18	V.ANUPARVATHI VASUDHA SASIKUMAR NEHA MAKATTIL VARUN ARYA	AVINYA, 2023 Technology Day	VESIT	SECOND PRIZE	Intracollege	20th March, 2023
19	AKSHATA DESHPANDE SHREYA DHAMANE GARGI KHANVILKAR GUNJAN SUREKA	AVINYA, 2023 Technology Day	VESIT	SECOND PRIZE	Intracollege	20th March, 2023
20	AISHVARYA MAHADEO BIRAMBOLE RISA SAMANTA ISHA SUDHIR CHAVAN SAAKSHI TEJPAL KARKERA	AVINYA, 2023 Technology Day	VESIT	Participation	Intracollege	20th March, 2023
21	KARTIK MAHADEV AVHAD	AVINYA, 2023 Technology	VESIT	Participation	Intracollege	20th March, 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

	ISHAN KHARAT SUMIT TUKARAM JADHAV DHAVAL UNMESH MAHAJAN	Day				
22	Rohit Dahad Kopal Jain Rahul Jeswani Yashraj Kadam	AVINYA, 2023 Technology Day	VESIT	Participation	Intracollege	20th March, 2023
23	PRAJNA MOORTHY MRUNAL MURUDKAR MCVEAN SOANS SAPTARSHI DAS	AVINYA, 2023 Technology Day	VESIT	Participation	Intracollege	20th March, 2023
24	ADARSH PS ROHAN GIRI BHAVESH KHATI SHRIPAD KULKARNI	AVINYA, 2023 Technology Day	VESIT	FIRST PRIZE	Intracollege	20th March, 2023
25	Paritosh Borkar Aditya Jagadale Aayush Moza Shreya kate	AVINYA, 2023 Technology Day	VESIT	Participation	Intracollege	20th March, 2023
26	AARON VARGHEESE ARIMPUR GARGI VINOD GAIKWAD RAJ JADHAV NIKHIL KUMAR	AVINYA, 2023 Technology Day	VESIT	Participation	Intra College	20th March, 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

	BHARAT KUMAR JAIN					
27	AMEYA DIKSHIT AHONA C. PRATHAM A. JANHAVI BHUTKI	AVINYA, 2023 Technology Day	VESIT	Participation	Intracollege	20th March, 2023
28	Abhishek Bhatia Avantika Shetty Shweta Bhabal Nafees akhter	AVINYA, 2023 Technology Day	VESIT	Participation	Intracollege	20th March, 2023
29	ARUNKUMAR MARIAPPAN HEERAL C. RIDDHI JOSHI FARHEEN KHAN	AVINYA, 2023 Technology Day	VESIT	THIRD PRIZE	Intracollege	20th March, 2023
30	YASH BHISE HRIDESH KAMAL Tanya Dubey Sagar Pujari	AVINYA, 2023 Technology Day	VESIT	FIRST PRIZE	Intracollege	20th March, 2023
31	Priyal Singh Dhwani Panchal Dhara Panchal Riya Gorade	IDEATHON, 2023	VESIT	Participation	Intracollege	8th April, 2023
32	Gaurang Kedare Omkar Maurya Sarthak Adhanagale Pooja Narayanan	IDEATHON, 2023	VESIT	Participation	Intracollege	8th April, 2023
33	Rohit Dahara Tejas Sharma Manish Mhatre Aditya Kurdiya	IDEATHON, 2023	VESIT	Participation	Intracollege	8th April, 2023
34	SHEEZA KHAN ADITYA NAMA	IDEATHON, 2023	VESIT	Participation	Intracollege	8th April, 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

	PRANAV GOPAL SUKALI SHRAAVANI TOPLE					
35	Pratham kumar Bumb Pritam Dhamak Aadarsh Dubey Swaraj Muraskar	IDEATHON, 2023	VESIT	Participation	Intracollege	8th April, 2023
36	Renu Dharamkar Atharva D. Anuraag Rao Aman yadav	IDEATHON, 2023	VESIT	FIRST PRIZE	Intracollege	8th April, 2023
37	Garima Chhabra Rishika Chaubal Hritika Mulay Sharvari Patwardhan	IDEATHON, 2023	VESIT	Participation	Intracollege	8th April, 2023
38	Dsouza Alrich Pratik Patkar Atharva Petkar Aayushi Rajeshirke	IDEATHON, 2023	VESIT	Participation	Intracollege	8th April, 2023
39	Tanmayee Bochare Kaustubh Pawar Shantanu Deshmukh Milind Sawant	IDEATHON, 2023	VESIT	Participation	Intracollege	8th April, 2023

11. Alumni Interaction

Sr. No.	Name of Alumni	Passing Year	Designation	Date	Target Audience	Topic of Discussion
---------	----------------	--------------	-------------	------	-----------------	---------------------



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

1	Mr. Anish Singh Rajput	ME-2017	Indian Telecom service (ITS) Officer, in the Department Of Telecommunication, Ministry of communication, Government of India.	1st April, 2023	Batch of 2021-2022	Convocation
2	Priyanka Pathak, Bhuvana Raisinghani, Kaustubh Salve, Aanand Lathi, Onkar Joshi, Saheb Wadhawa, Shubham Singh, Neha Nishad, Siddhant Satam, Shubham Madhavi, Gaurav Jagwani, Sunit Raut	Alumni from various batches	Working with various Indian/MNC's in Core technology/IT	8th April, 2023	Review of Second and Third year Mini Projects	Judging and Interaction

12. Co-curricular activities

12.1. Inter college competitions

Sr.	Name of the student	College	Name of the event	Awards/
-----	---------------------	---------	-------------------	---------



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

No.	Name	Name	Participant	
1	Satyam Mishra	TSEC	Tech-a-thon(48hr Hardware Hackathon)	Participant
2	Yash Kumbhar	TSEC	Tech-a-thon(48hr Hardware Hackathon)	Participant
3	Manashree Tendulkar	TSEC	Tech-a-thon(48hr Hardware Hackathon)	Participant

12.2. Intra college competitions

Sr. No.	Name of the Student	College Name	Name of the Event	Awards/ Participant
1	Pooja Narayanan	VESIT	Article writing (2nd position), ISA	Winner
2	Devesh Ramesh	VESIT	Code Knights, CSI	Participant
3	Devesh Ramesh	VESIT	Sherlock & Watson, CSI	Participant
4	Trupti Sonawane	VESIT	LBS - Looking Beyond Syllabus	Participant
5	Kaustubh Vijay Pawar	VESIT	LBS - Looking Beyond Syllabus	Winner
6	Kaustubh Vijay Pawar	VESIT	Ideathon	Participant
7	Kshitij Nag	VESIT	LBS - Looking Beyond Syllabus	Participant
8	Aditya Naresh	VESIT	LBS - Looking Beyond Syllabus	Participant
9	Omkar Mourya	VESIT	Ideathon	Participant
10	Vismay Kakhandki	VESIT	LBS - Looking Beyond Syllabus	Participant
11	Mcvean Soans	VESIT	VESIT Hacks	Participant
12	Mcvean Soans	VESIT	Syrus 2023	Winner
13	Farheen Khan	VESIT	Praxis , VESIT hacks	Winner
14	Shantanu Nilesh Deshmukh	VESIT	LBS - Looking Beyond Syllabus	Winner



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

15	Manashree Tendulkar	VESIT	LBS - Looking Beyond Syllabus	Participant
16	Prachi Thombare	VESIT	LBS - Looking Beyond Syllabus	Participant
17	Pooja Narayanan	VESIT	Ideathon	Participant
18	Riya Gorade	VESIT	Ideathon	Participant
19	Udit Koli	VESIT	LBS - Looking Beyond Syllabus	Participant
20	Gaurang Kedare	VESIT	Ideathon	Participant
21	Devesh Ramesh	VESIT	VESIT Tank, IEEE	Participant
22	Aayushi Mishra	VESIT	Sherlock & Watson, CSI	Participant
23	Rahul lad	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
24	Shravani Kale	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
25	Aditi Ullas	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
26	Anuja Kulkarni	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
27	Siddhesh Shirshivkar	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
28	Farhan khan	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
29	Sanika Pawar	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
30	Harshal Talreja	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
31	Chirag Naskar	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
32	Soham Narvekar	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
33	Neerav Sonaje	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

34	Aaroh Srivastava	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
35	Unnati Ayalwar	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
36	Geet Parag Lad	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant
37	Yash Bamne	VESIT	Technical Paper Presentation, VESIT-IEEE	Participant

13. Extracurricular activities

13.1. Sports events

Sr. No.	Student Name	Event Name
1	Mohit Sajeevan	Shri. Ram Keswani Football Cup
2	Varun Arya	Inter-VES Tug of war
3	Maaroor Merchant	Inter-VES Tug of war
4	Varun Arya	Sphurti'23
5	Pratham Patil	Vesit Football League Winner
6	Aniket Sangle	Spurti table tennis winner
7	Aniket Sangle	Spurti Volleyball Winner
8	Varun bangad	Volleyball runner up
9	Aryan Chaudhary	Volleyball Runner up
10	Harsh Ranjan	Badminton singles winner
11	Harsh Ranjan	Badminton mixed doubles winner
12	Harsh Ranjan	Table tennis doubles winner
13	Harsh Ranjan	volleyball runner up
14	Harsh Ranjan	volleyball winner
15	Harsh Ranjan	kho kho runner up
16	Harsh Ranjan	VCL winner
17	Harsh Ranjan	Badminton singles winner and MVP in Inter Ves tournament
18	Harsh Ranjan	Relay 4*100 runner up inter ves



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

19	Harsh Ranjan	Volleyball runner up inter ves
20	Harsh Ranjan	Tug of war runner up inter ves
21	Pooja Narayanan	Dodgeball
22	Jayesh Utekar	Inter VES Kabbadi winner
23	Jayesh Utekar	Sphurthi Volleyball runner-up
24	Jayesh Utekar	Illusion wild card event runner up
25	Anoushka Menon	Indoor Spurthi
26	Sakshi Gawale	Indoor Spurthi and Outdoor Sphurthi
27	Aditi Boob	Indoor Spurthi and Outdoor Sphurthi
28	Rohit Kumar Singh	Outdoor Spurthi
29	Prathmesh Pramod Bhingarde	Outdoor Spurthi
30	Thanmai Chundururu	outdoor Spurthi
31	Unnati Ayalwar	Outdoor Spurthi
32	Sumit Sanjiv Pawar	Outdoor Spurthi
33	Vikas Atmaram Dhakane	Outdoor Spurthi
34	Rohan Jeevan Chavan	Outdoor Spurthi
35	Sumit Jadhav	UBI * VESLIT Drama

13.2. Cultural, music, literature

Sr. No.	Student Name	Event Name
1	Hritika Mulay	VESLit X UBI Debate
2	Puja Mahankuda	Essay writing competition conducted by the VESIT student council (first place)
3	Sumit Jadhav	UBI * VESILT Drama
4	Sudipa Majumdar	Octaves'23 (Voice of VESIT)
5	Sudipa Majumdar	Octaves'23 (Battle of Bands)
6	Saakshi Karkera	UBI - Poster Making Competition
7	Jayesh Utekar	Illusion - Best MC (Main Captain)
8	Jayesh Utekar	Illusion- 1st in Duet Drama
9	Jayesh Utekar	Illusion - 2nd in Group Drama
10	Ayush Kotkar	Illusion - 2nd in Music



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

11	Srushti Pagar	Illusion'23 Winning team (1st place as dance mc)
12	Harsh Ranjan	Illusion winner (played as dual in sports and drama)
13	Hritika Mulay	VESLit X UBI Debate

14. Tinkerer's Lab Activities

Sr. No.	Attended By	Course description	Organized by	Date
1	Prathamesh bhalchandra chavan	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
2	Rohankumar Barouliya	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
3	Suja Ravindran	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
4	Varun Tushar Gharat	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
5	Pooja Ramchandra Ghadage	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
6	Indu Dokare	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
7	Vaibhav	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

8	SHREYAS SANTOSH SARANG	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
9	Prasad Kokate	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
10	Prachi Thombare	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
11	Yash Shivyogappa Kumbhar	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
12	Uttara Bhatt	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
13	Siddhi Kadam	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
14	Jay Dulange	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
15	Shubham Nayak	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
16	Harsh Shrivastava	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

17	Sanchit Mhadgut	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
18	MIHIR SUHANDA	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
19	SAWANT SHREYA DEEPAK	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
20	Radhika Damle	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
21	Avantika Ravindra Narvekar	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
22	Geet Lad	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
23	Ayush Kotkar	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
24	Pradeesh Reddy	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
25	Manasi Nerurkar	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

26	Atharva Godkar	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
27	Udit koli	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
28	Shah Jash Bharat	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
29	Manpreat Kaur	STTP on "Trends in FPGA design and Prototyping using Verilog"	Tinkerer's LAB, EXTC VESIT	2nd - 7th Jan 2023
30	Aditya Nama	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
31	Nandana Nair	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
32	Adwait Panadi	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
33	Sakshi Santosh Patil	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
34	Komal Tilokani	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
35	Vedant Gupta	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
36	Vedika Vinod Chavan	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
37	Mayuri Yerande	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
38	Pushkaraj Charudatta Chaudhari	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
39	Anuja Kulkarni	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
40	Aditi Ullas	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

41	Manali Bhave	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
42	Preeti Sonawane	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
43	Rohini Daultani	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
44	Kesar Assudani	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
45	Ishan Somani	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
46	Jiya Santani	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
47	Sharvaree pimparkar	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
48	Sakshi Gawale	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
49	Garima Chhabra	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
50	VIKAS A. DHAKANE	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
51	Aditri Vishwas	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
52	Akash Neharkar	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
53	Ameya Chaudhary	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
54	Nihit Mistry	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
55	Soumyaranjan Parhi	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
56	Aditya Gaikwad	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
57	YASH SHIVAJI KURADE	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
58	Prajwal Vijay Patil	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
59	Vivek Jambhulkar	Prototyping with	Tinkerer's LAB,	21,28-Jan to



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

		Raspberry-pi	EXTC VESIT	Feb-2023
60	Nayan Sunil Patil	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
61	Ratan Poojari	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
62	Sarvadnya Awaghad	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
63	Pranjali Patil	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
64	Riddhi Solanke	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
65	Atharva Dabhade	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
66	Suyog Bharsakle	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
67	Sahil Waykole	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
68	Adheeti Dalal	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
69	Ankit Balwani	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
70	Vishal singh	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
71	Atharva Santosh Mulam	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
72	Palak Lalwani	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
73	Rohit Dahara	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
74	Jalpriya	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
75	Mayur Pawar	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
76	Ghanishtha Talele	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
77	Atharva Godkar	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

78	Aditi Miniyar	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
79	Vaishnavi Chavan	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
80	Prathamesh Savalkar	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
81	Gayatri Vaidya	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
82	Rohit kumar singh	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
83	ADITYA BAGAYATKAR	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
84	Nikita kumawat	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
85	Manish Mhatre	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
86	Ninad Joshi	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
87	Aaqueeb Pinjari	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
88	Gaurang Raorane	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
89	Samiksha Wadibhasme	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
90	Janhavi Deshpande	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
91	Vrushabh Ghuse	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
92	Unnati Ayalwar	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
93	Gaurav	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
94	Dheeraj Yadav	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
95	Shubham Raghuvir Nakashe	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
96	Mahendra Girase	Prototyping with	Tinkerer's LAB,	21,28-Jan to



Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

		Raspberry-pi	EXTC VESIT	Feb-2023
97	Piyush Tilokani	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
98	Hritika Mulay	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
99	Renu Dharamkar	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
100	Aditi	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
101	Aatish Salian	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
102	Harshita Dubey	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
103	SNEHA SANDEEP UTEKAR	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
104	Tejas Sharma	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
105	Sakshi Ganjewar	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
106	Rishika Chaubal	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023
107	Swaraj Muraskar	Prototyping with Raspberry-pi	Tinkerer's LAB, EXTC VESIT	21,28-Jan to Feb-2023